VME Load Board

Technical Manual

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Control Cabinet

In the context of this user manual, the control cabinet must fulfill the requirements on fire-protective enclosures according to EN 60950 / IEC 60950 / UL 60950.
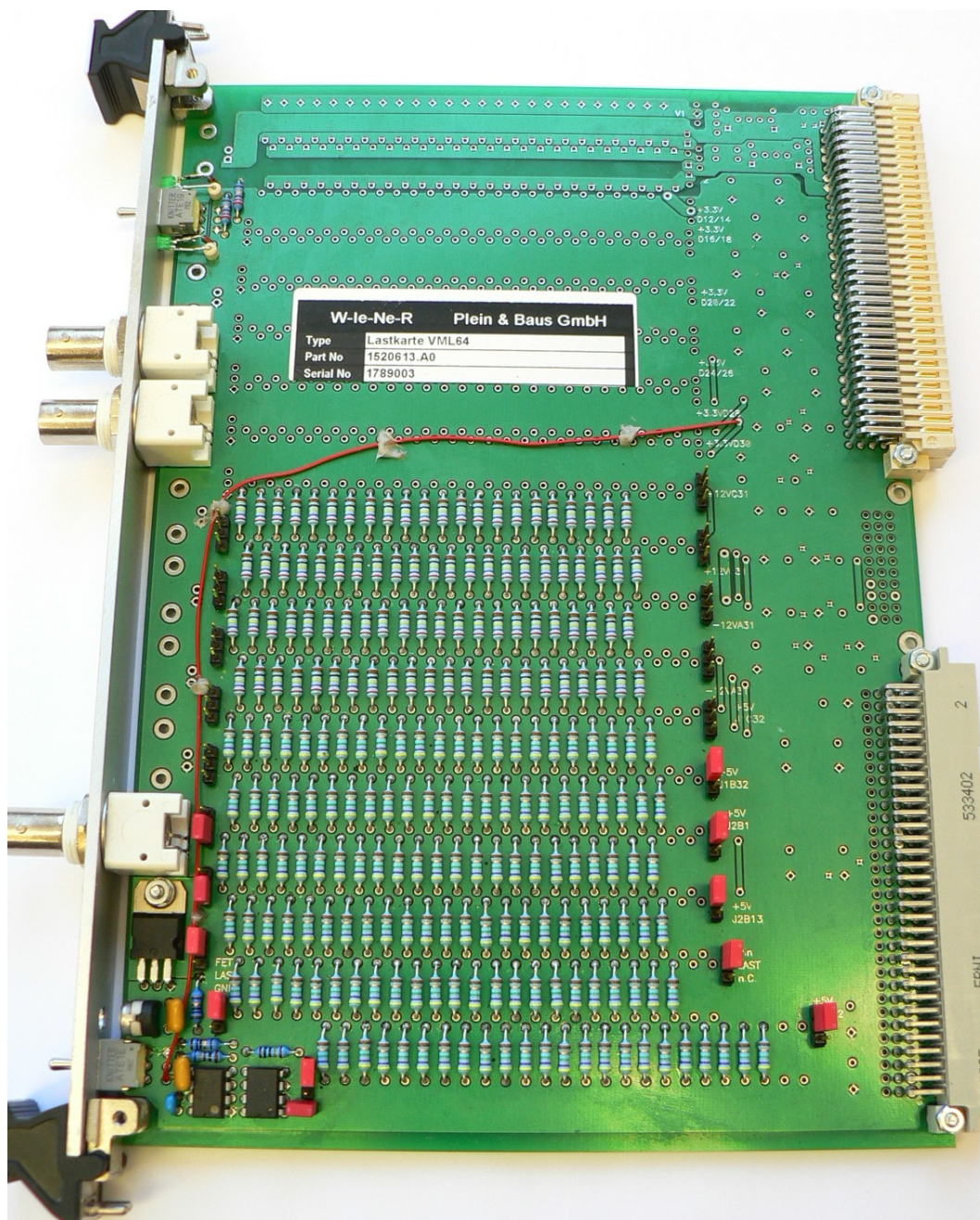
All devices are intended for operation in control cabinets or in closed areas. The LAN connection and all wire connections between the different system parts must be done via shielded cable with conductive connector shells, which are fixed with screws.

Furthermore, an additional fire-protective enclosure is required which must not affect proper air circulation.
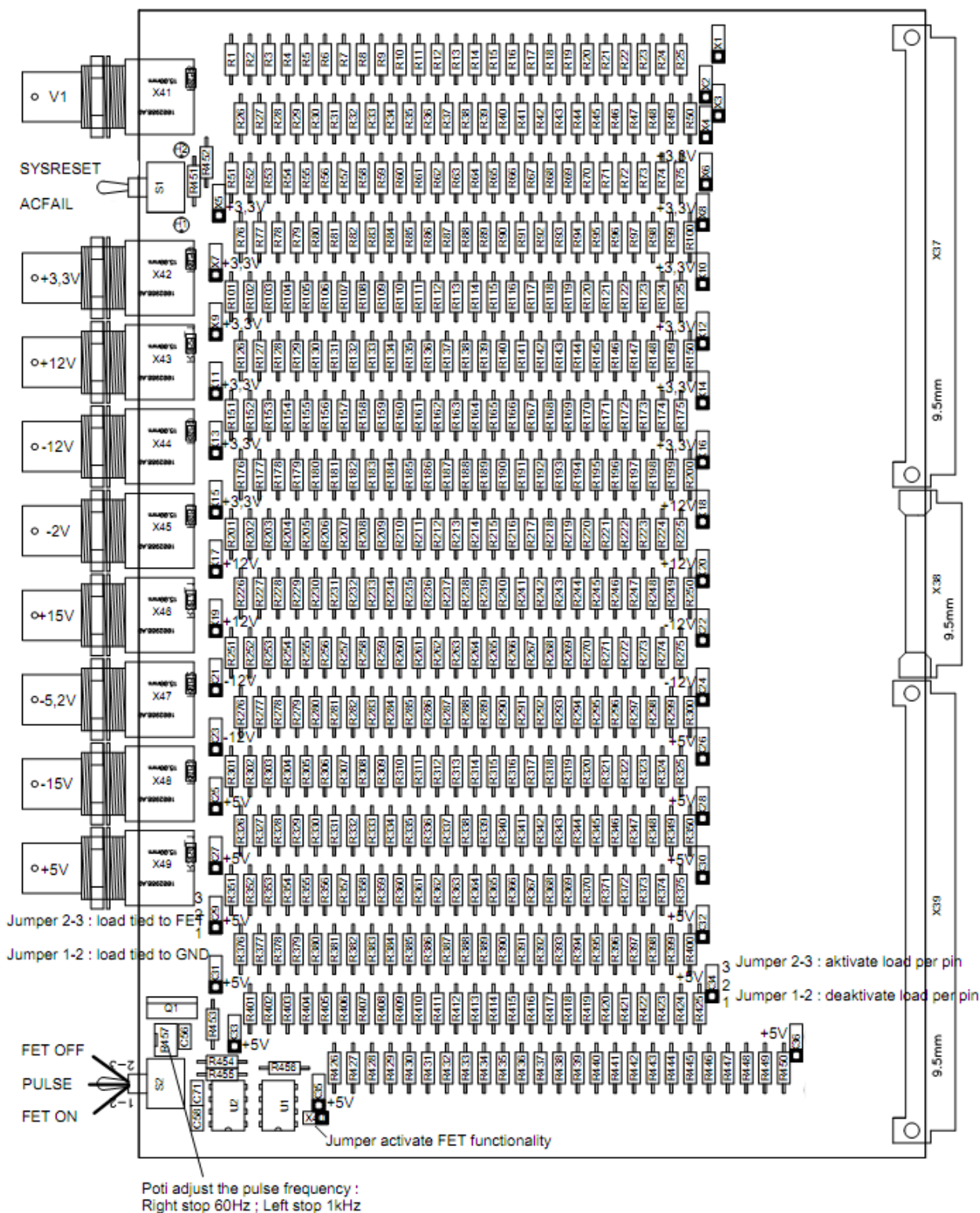
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1 General Information

VML-64

WIE·NE·R
Plein & Baus Elektronik

Werk für
Industrie-elektronik
Nuklear-elektronik
Regelungs-technik

V1

SYSRESET
ACFAIL

o +3,3V

o +12V

o -12V

o -2V

o +15V

o -5,2V

o -15V

o +5V

Jumper 2-3 : load tied to FET
Jumper 1-2 : load tied to GND

FET OFF
PULSE
FET ON

Poti adjust the pulse frequency :
Right stop 60Hz ; Left stop 1kHz

Jumper 2-3 : aktivate load per pin
Jumper 1-2 : deaktivate load per pin

Jumper activate FET functionality

Picture 3

Features

- **Front Panel switches to control current load**
- **Up to 10A load per board for 5V and 3,3V**
- **Up to 3A load per board for +12V and -12V**

- **Different versions for each VME specification to test every voltage**
- **High power versions available for testing high power 5V systems**
- **LED indicator lights to show current draw**

The WIENER VME load boards are designed to test VME crate power supplies and fan trays by drawing power from the VME backplane and dissiplating it as heat. A series of jumper on the module allows for easy adjustment of the current draw for each voltage channel.

By monitoring exhaust air temperature above a VME load board it is possible to locate VME slots with cooling issues. VME load boards also provide an easy way to test the maximum current of a crate power supply and to confirm current trip points behave as expected.

2 Front Panel and Board Elements

2.1 SYSRESET and ACFAIL Switch

This switch actuate an SYSRESET or ACFAIL signal.

FET Switch

Switch upper position : The transistor (FET) is OFF (high-impedance)

Switch middle position : The transistor (FET) pulsed

(frequency adjustment between 60Hz...1kHz)

Switch lower position : The transistor (FET) is ON (low-impedance)

The load is tied to ground (GND)

2.2 Poti

With the poti you can adjust the frequency of the FET.

Right stop : 60Hz

Left stop : 1kHz

2.3 Jumper (see picture 3)

With the jumper on the right side you can select the load per pin.

With the jumper on the left you can differentiate the load between tie the selected load to ground or tie the load to the FET functionality.